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LSI Logic Corporation
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Milpitas, CA 95035

Date December 8, 2005
Number of pages including cover sheet 2

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TC 2128
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From: Connie del Castillo
Intellectual Property Paralegal
Telephone (408) 433-7191
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REMARKS:

Urgent For your review Reply ASAP Please comment

Application Number: 09/344,169
Filing date: June 24, 1999
First named inventor: William Eric Corr
Attorney docket number: P-3376 / 30454-230

Transmitted herewith for filing via facsimile:

- Status Request Letter

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

12/8/05Connie del Castillo

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PAGE 1/2 * RCVD AT 12/8/2005 5:51:04 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-6/36 * DNIS:2738300 * CSID:408 433 7460 * DURATION (mm:ss):00:38

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

William Eric Conn

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Serial No. : 09/344,169

Group Art Unit : 2128

Filed : June 24, 1999

§ Examiner : Jones, Hugh

For : Determining Timing of Integrated Circuits

§ Atty Docket : 30454-230 / P-3376

STATUS REQUEST

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:-

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

Respectfully Submitted,

1099

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Reg. No. 43,331

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Date: 11/5/05

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